

## **ABSTRACT OF THE DISCLOSURE**

The present invention pertains to a BIOS debug method, which adds an error output command (a command that outputs a particular to a debug port) at a proper position in each sub-routine of a power on self test (POST) and inserts a condition before the error output 5 command so that only when the condition holds will the error output command be compiled and executed. The place to put in the error output command can be when there is more than one setting or initialization hardware devices or a test step is too tedious. Through the condition, the enable state of the error output command can be controlled to be enable or disable, whereby the BIOS is determined to be a test version or a final version. The invention 10 thus provides a test operation in manufacturing that can correctly find out the hardware or cause that results in test errors without affecting the BIOS execution efficiency under normal uses.

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